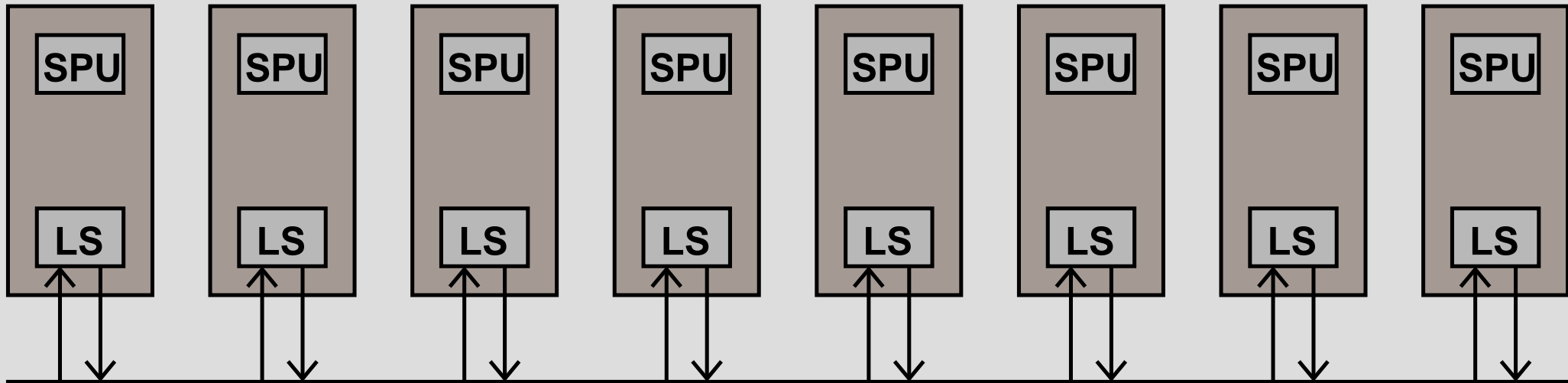
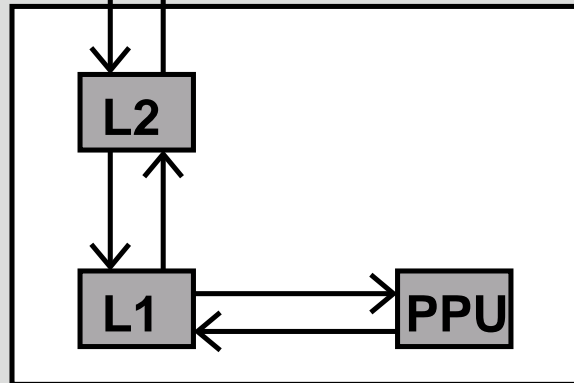


Synergetic Processing Elements

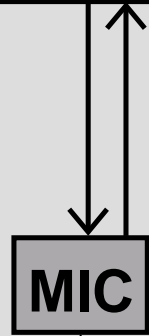


16B/
Zyklus

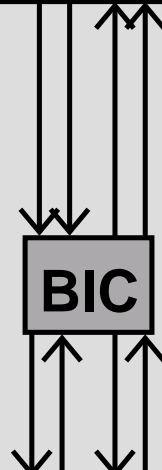
EIB (bis 96 B/Zyklus)



64-Bit Power Architektur



Dual
XDR



RRAC I/O