

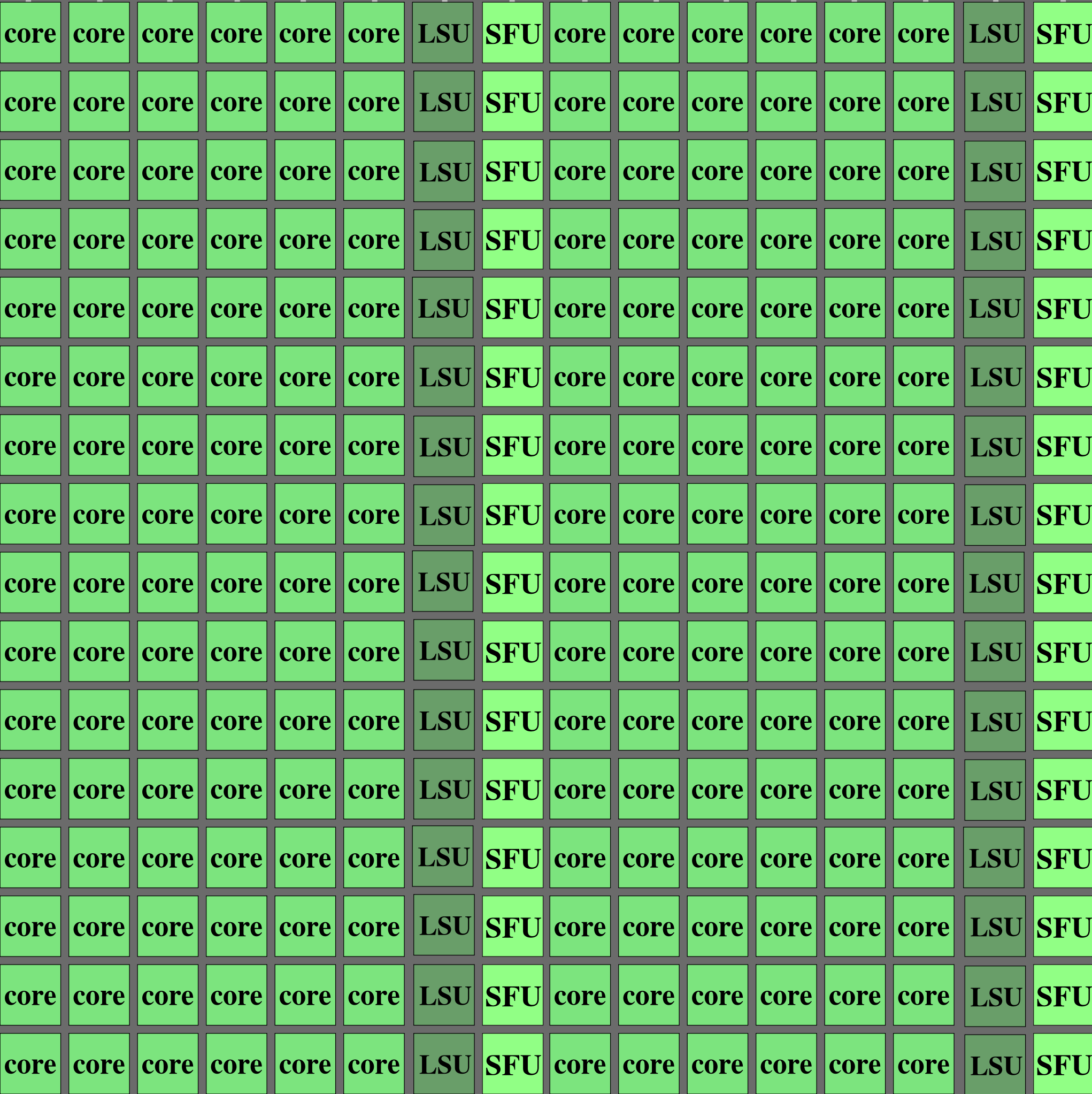
**PolyMorph Engine 2.0**

**instruction cache**

**SIMD Thread Scheduler**    **SIMD Thread Scheduler**    **SIMD Thread Scheduler**    **SIMD Thread Scheduler**

**dispatch unit**    **dispatch unit**    **dispatch unit**    **dispatch unit**    **dispatch unit**    **dispatch unit**    **dispatch unit**    **dispatch unit**

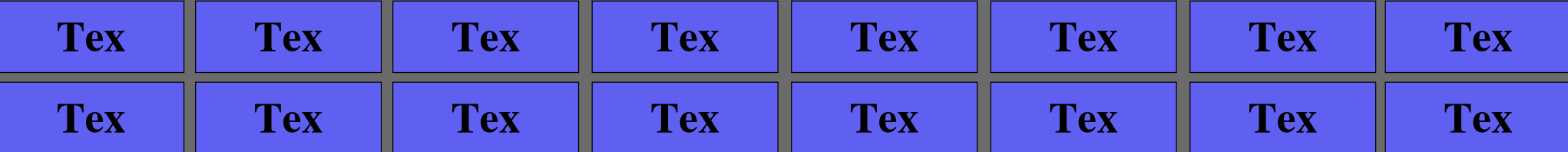
**register file (65.536 x 32-bit)**



**texture cache**

**64 KB Shared Memory/L1-Cache**

**L2-Cache shared by all SIMD processors**



**interconnection network**